

FIG. 1

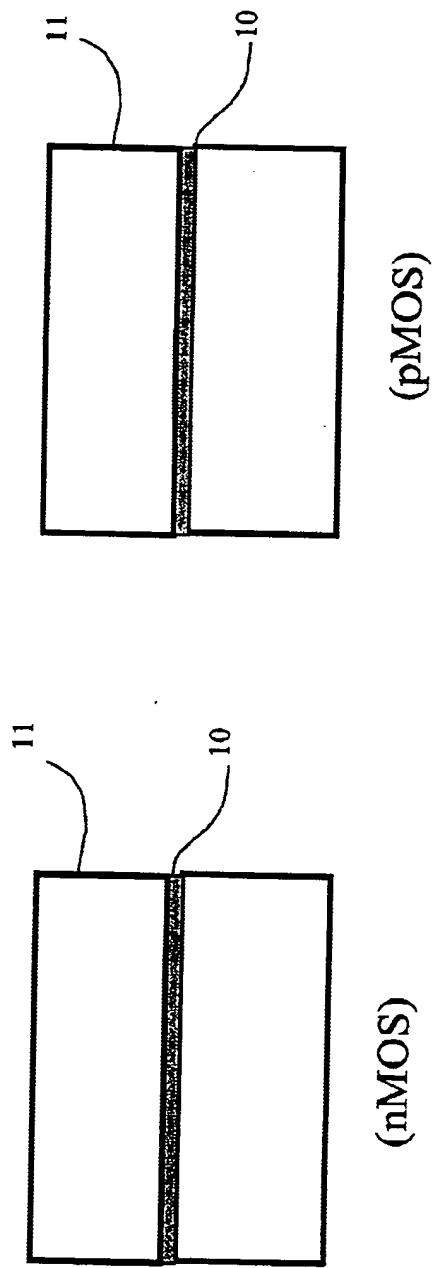
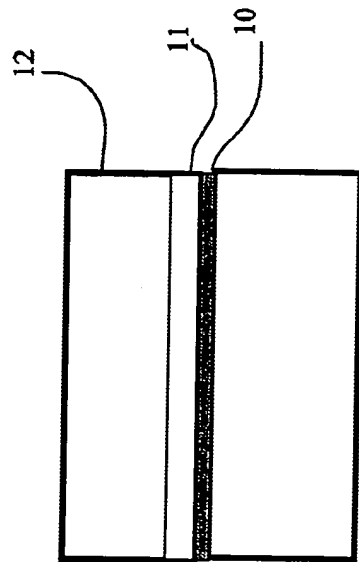
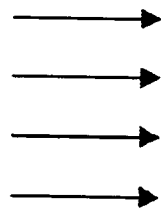
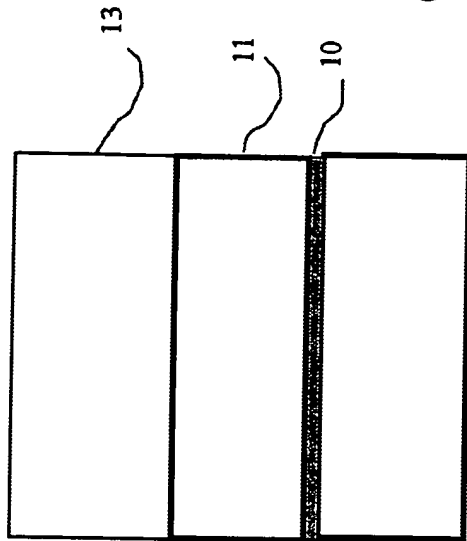


FIG. 2



(nMOS)



(pMOS)

FIG. 3

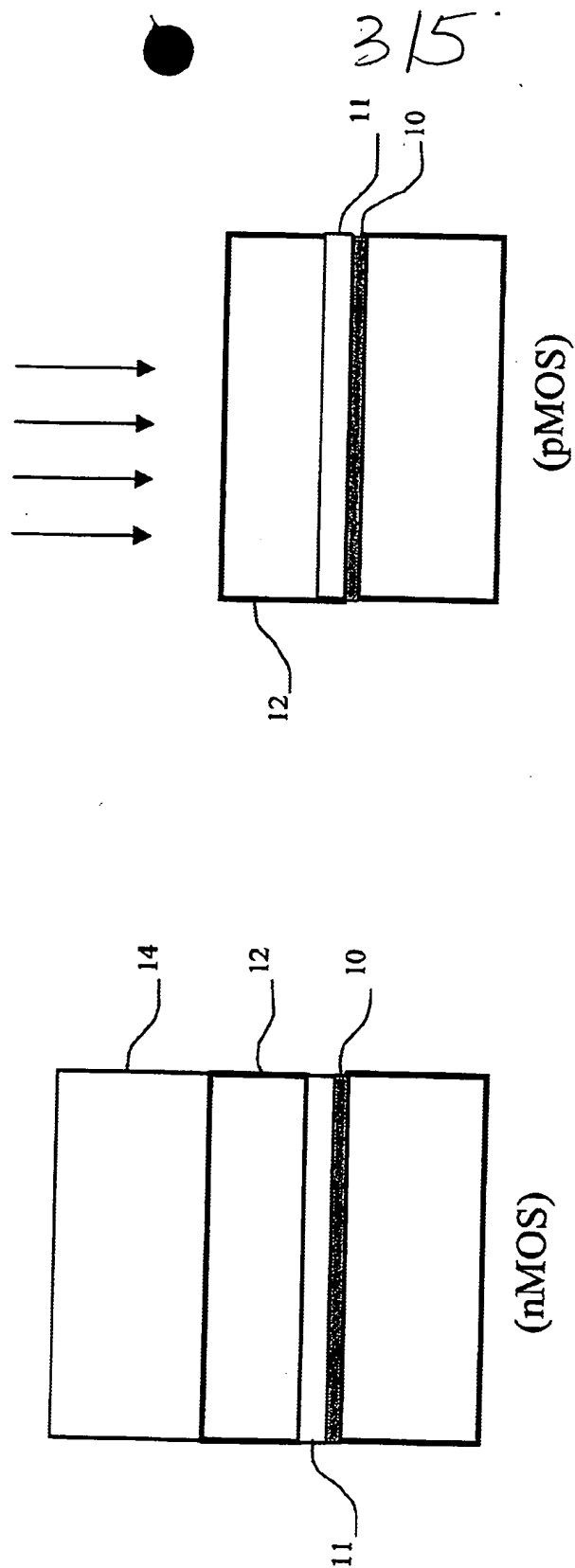


FIG. 4

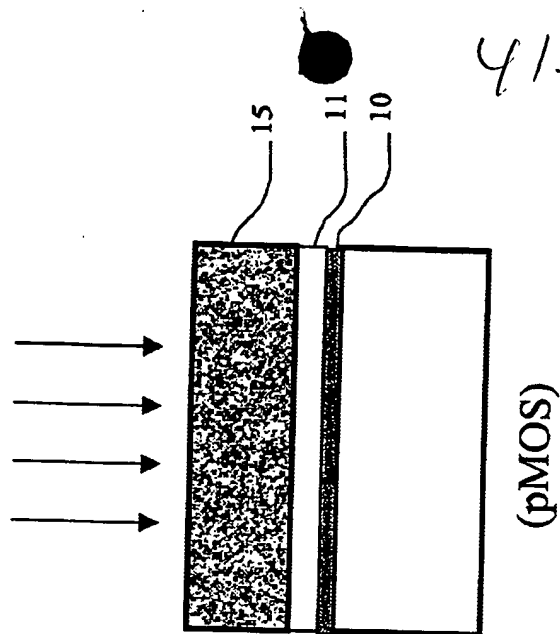
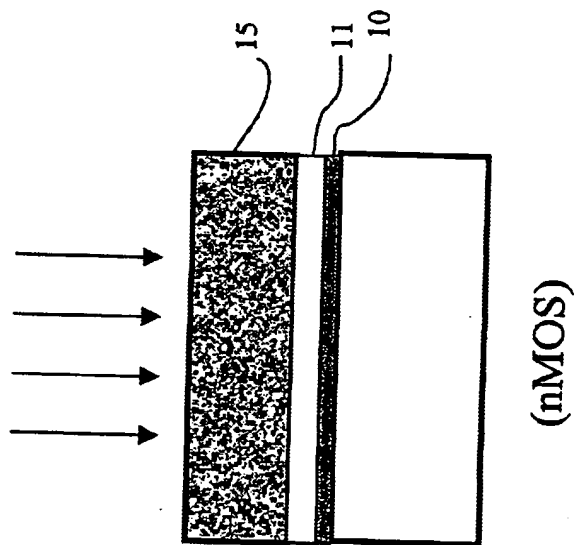
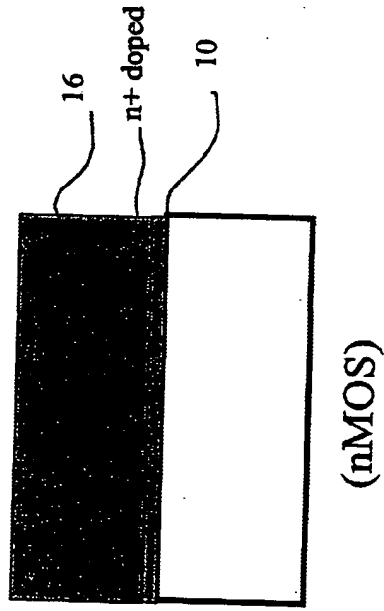
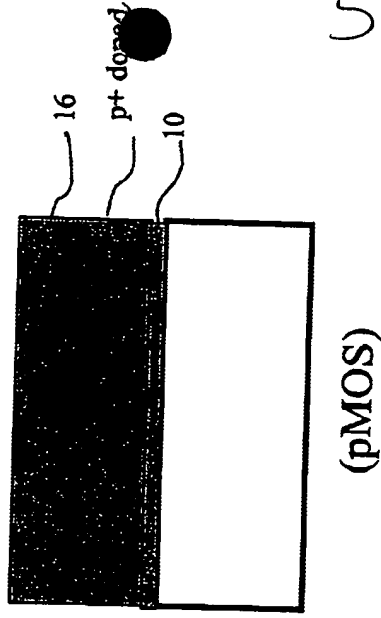


FIG. 5

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(nMOS)



(pMOS)

FIG. 6

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